

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF:

SHINOHARA et al.

SERIAL NO:

10/690,316

FILED:

October 21, 2003

FOR:

SEMICONDUCTOR MEMORY AND PROCESS FOR

FABRICATING THE SAME

GROUP:

2818

EXAMINER:

LE, Thao P.

DOCKET: NEC 19851 DIV

MAIL STOP ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CORRECTED NOTICE OF ALLOWABILITY

Dear Sir:

The Notice of Allowability mailed December 21, 2004 contains an error at cipher 4.

The claim for foreign priority is not acknowledged in the Notice. A certified copy of Japanese Patent Application No. 2001-075485 is of record in the parent Application, U.S. Patent No. 6,730,955.

It is respectfully requested that a Corrected Notice of Allowability be issued.

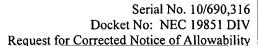
Respectfully submitted,

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CERTIFICATE OF MAILING

I certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: MAIL STOP - ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on at Tucson, Arizona.

By: Kim Hord

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